

General purpose (dual digital transistors)

EMD4 / UMD4N

●Features

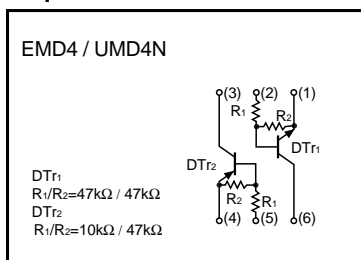
- 1) Both the DTA114Y chip and DTC144E chip in an EMT6 or UMT6 package.
- 2) Mounting possible with EMT3 or UMT3 automatic mounting machines.
- 3) Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

●Structure

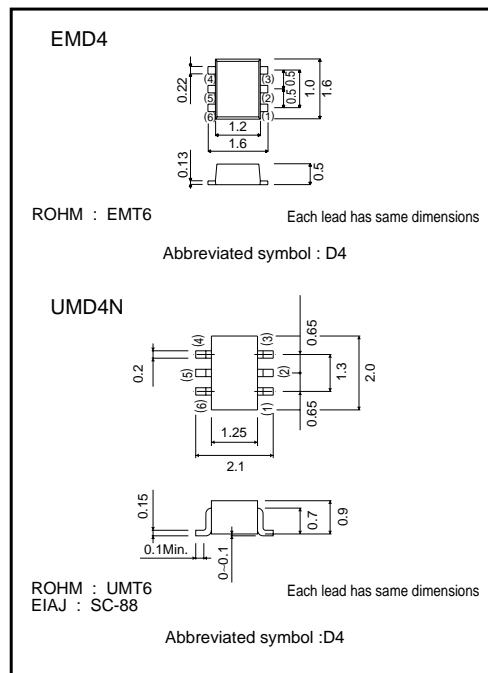
A PNP and NPN digital transistor
(each with a single built in resistor)

The following characteristics apply to both the DTr1 and DTr2, however, the “-” sign on DTr2 values for the PNP type have been omitted.

●Equivalent circuit



●External dimensions (Unit : mm)



●Packaging specifications

| Type | Package | Taping | |
|-------|------------------------------|--------|------|
| | Code | T2R | TR |
| | Basic ordering unit (pieces) | 8000 | 3000 |
| EMD4 | | ○ | — |
| UMD4N | | — | ○ |

Transistors

●Absolute maximum ratings (Ta = 25°C)

| Parameter | Symbol | Limits | | Unit |
|----------------------|----------------------|---------------|---------------|------|
| | | DTr1(DTC144E) | DTr2(DTA114Y) | |
| Supply voltage | V _{CC} | 50 | -50 | V |
| Input voltage | V _{IN} | -10 to +40 | -40 to +6 | V |
| Output current | I _O | 30 | -70 | mA |
| | I _{C(Max.)} | 100 | -100 | |
| Power dissipation | P _d | 150(TOTAL) | 120(1ELEMENT) | mW |
| Junction temperature | T _j | 150 | | °C |
| Storage temperature | T _{stg} | -55 to +150 | | °C |

●Electrical characteristics (Ta = 25°C)

DTr 1

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Conditions |
|----------------------|--------------------------------|------|------|------|------|--|
| Input voltage | V _{I(off)} | - | - | 0.5 | V | V _{CC} =5V, I _O =100μA |
| | V _{I(on)} | 3 | - | - | | V _O =0.3V, I _O =2mA |
| Output voltage | V _{O(on)} | - | 0.1 | 0.3 | V | I _O /I _I =10mA/0.5mA |
| Input current | I _I | - | - | 0.18 | mA | V _I =5V |
| Output current | I _{O(off)} | - | - | 0.5 | μA | V _{CC} =50V, V _I =0V |
| DC current gain | G _I | 68 | - | - | - | V _O =5V, I _O =5mA |
| Input resistance | R _I | 32.9 | 47 | 61.1 | kΩ | - |
| Resistance ratio | R ₂ /R ₁ | 0.8 | 1 | 1.2 | - | - |
| Transition frequency | f _T | - | 250 | - | MHz | V _{CE} =10V, I _E =-5mA, f=100MHz * |

* Transition frequency of the device

DTr 2

| Parameter | Symbol | Min. | Typ. | Max. | Unit | Conditions |
|----------------------|--------------------------------|------|------|-------|------|---|
| Input voltage | V _{I(off)} | - | - | -0.3 | V | V _{CC} = -5V, I _O = -100μA |
| | V _{I(on)} | -1.4 | - | - | | V _O = -0.3V, I _O = -1mA |
| Output voltage | V _{O(on)} | - | -0.1 | -0.3 | V | I _O /I _I = -5mA / -0.25mA |
| Input current | I _I | - | - | -0.88 | mA | V _I = -5V |
| Output current | I _{O(off)} | - | - | -0.5 | μA | V _{CC} = -50V, V _I =0V |
| DC current gain | G _I | 68 | - | - | - | V _O = -5V, I _O = -5mA |
| Input resistance | R _I | 7 | 10 | 13 | kΩ | - |
| Resistance ratio | R ₂ /R ₁ | 3.7 | 4.7 | 5.7 | - | - |
| Transition frequency | f _T | - | 250 | - | MHz | V _{CE} = -10V, I _E =5mA, f=100MHz * |

* Transition frequency of the device

Transistors

●Electrical characteristic curves

DTr1 (NPN)

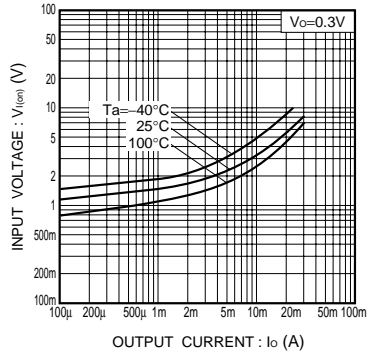


Fig.1 Input voltage vs. output current (ON characteristics)

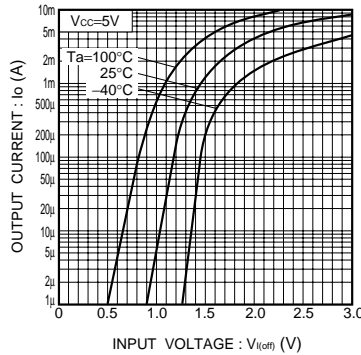


Fig.2 Output current vs. input voltage (OFF characteristics)

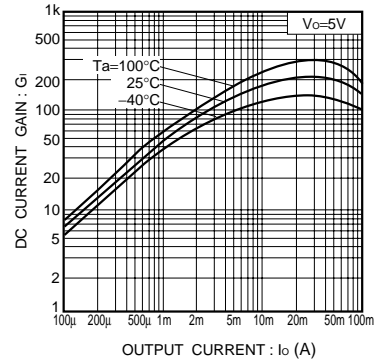


Fig.3 DC current gain vs. output current

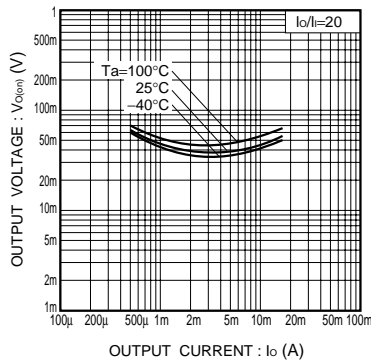


Fig.4 Output voltage vs. output current

DTr2 (NPN)

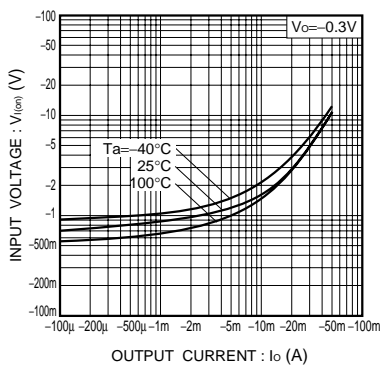


Fig.5 Input voltage vs. output current (ON characteristics)

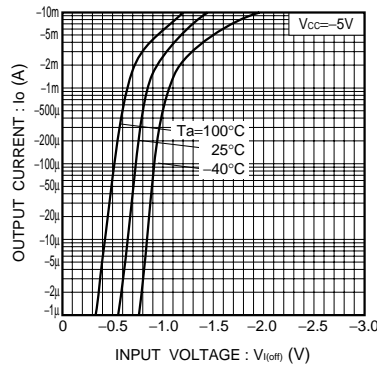


Fig.6 Output current vs. input voltage (OFF characteristics)

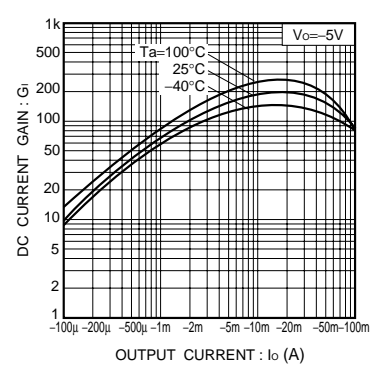


Fig.7 DC current gain vs. output current

Transistors

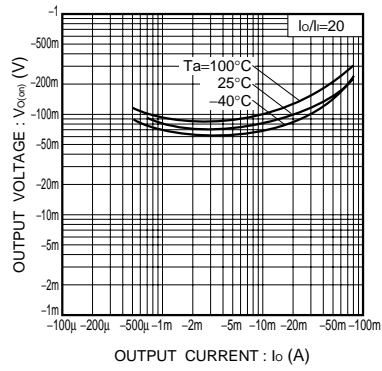


Fig.8 Output voltage vs. output current

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